



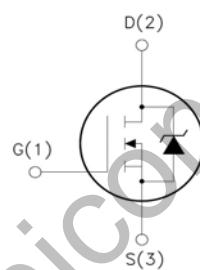
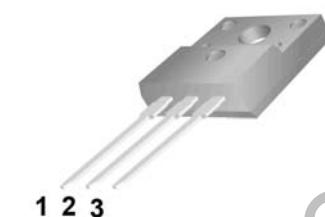
WGF30N10

100V N-Channel MOSFET

Features:

- Low Intrinsic Capacitances.
- Excellent Switching Characteristics.
- Extended Safe Operating Area.
- Unrivalled Gate Charge : $Q_g = 31\text{nC}$ (Typ.).
- $V_{BDSS} = 100\text{V}$, $I_D = 30\text{A}$
- $R_{DS(on)} : 0.07\Omega$ (Max) @ $V_G = 10\text{V}$
- 100% Avalanche Tested

TO-220F



1. Gate (G)
2. Drain (D)
3. Source (S)

Absolute Maximum Ratings* ($T_c = 25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	30	A
Drain Current-Continuous($T_c = 100^\circ\text{C}$)	$I_D(100^\circ\text{C})$	12	A
Pulsed Drain Current	I_{DM}	60	A
Maximum Power Dissipation	P_D	55	W
Single pulse avalanche energy <small>(Note 5)</small>	E_{AS}	250	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ\text{C}$

Thermal Characteristics

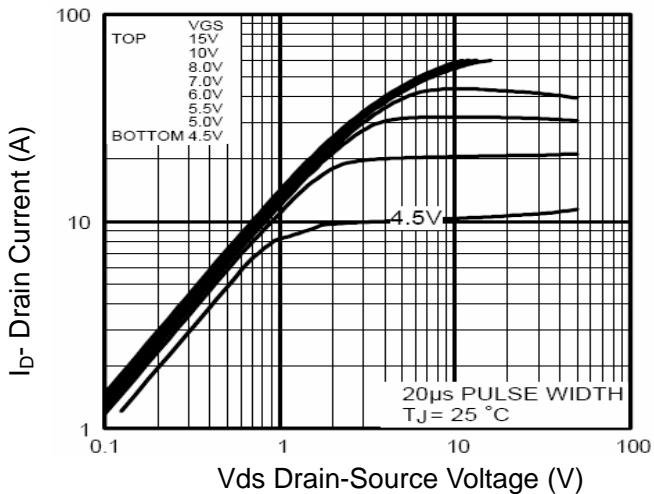
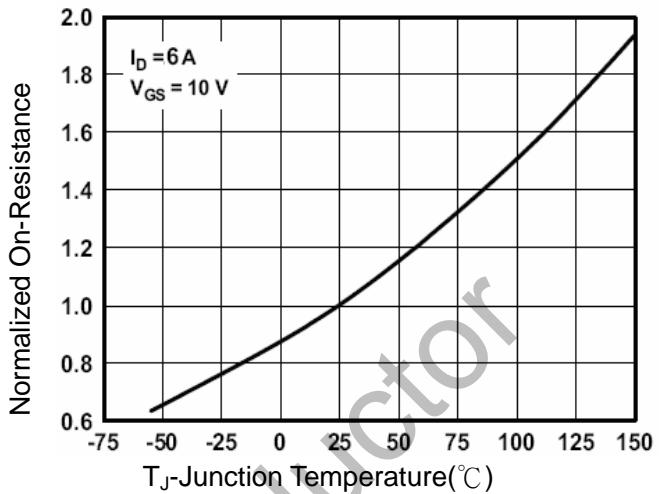
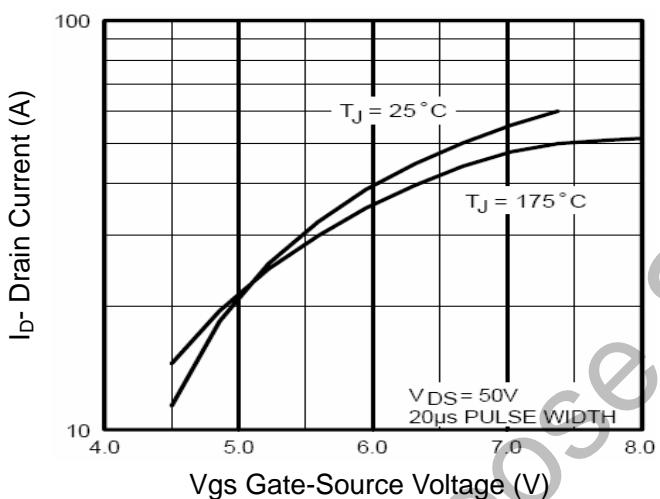
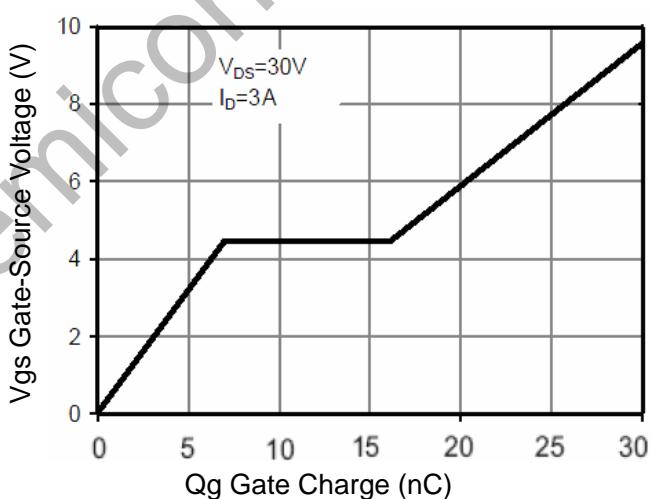
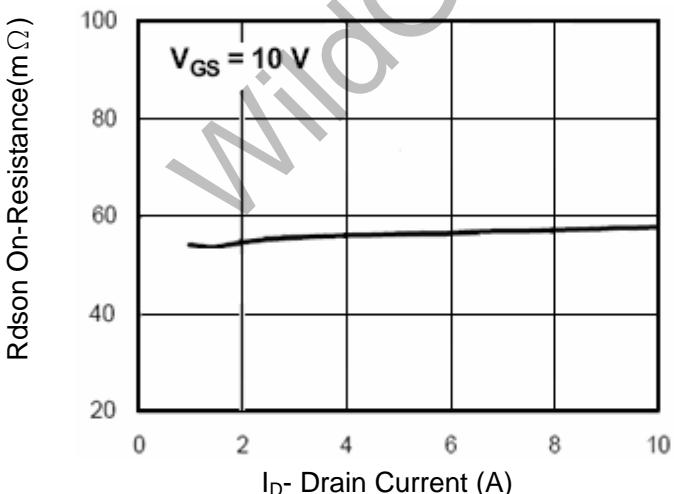
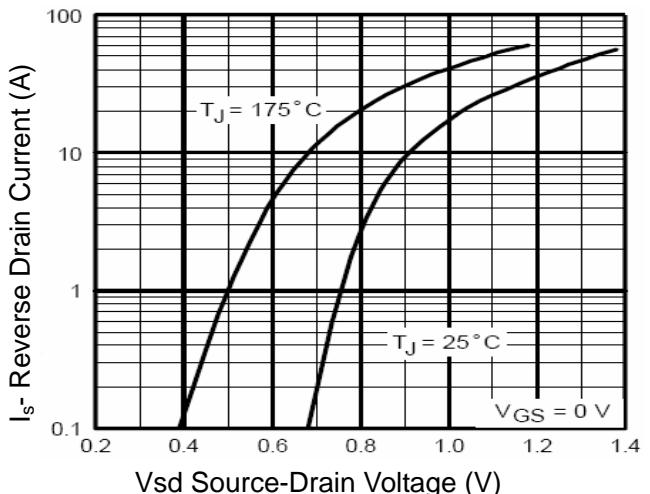
Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	2.27	$^\circ\text{C}/\text{W}$
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Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	100	110	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.8	2.5	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=5\text{A}$	-	56	70	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=9\text{A}$	12	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	1350	-	PF
Output Capacitance	C_{oss}		-	240	-	PF
Reverse Transfer Capacitance	C_{rss}		-	180	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=30\text{V}, I_{\text{D}}=2\text{A}, R_{\text{L}}=15\Omega$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=2.5\Omega$	-	13.8	-	nS
Turn-on Rise Time	t_{r}		-	9.3	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	43.8	-	nS
Turn-Off Fall Time	t_{f}		-	11.4	-	nS
Total Gate Charge	Q_{g}	$V_{\text{DS}}=30\text{V}, I_{\text{D}}=3\text{A}, V_{\text{GS}}=10\text{V}$	-	31	-	nC
Gate-Source Charge	Q_{gs}		-	6.4	-	nC
Gate-Drain Charge	Q_{gd}		-	9.4	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=9\text{A}$	-	-	1.2	V
Diode Forward Current (Note 2)	I_{S}		-	-	30	A
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition : $T_j=25^\circ\text{C}, V_{\text{DD}}=50\text{V}, V_{\text{G}}=10\text{V}, L=0.5\text{mH}, R_{\text{G}}=25\Omega$

Typical Electrical and Thermal Characteristics (Curves)**Figure 1 Output Characteristics****Figure 4 Rdson-JunctionTemperature****Figure 2 Transfer Characteristics****Figure 5 Gate Charge****Figure 3 Rdson- Drain Current****Figure 6 Source- Drain Diode Forward**

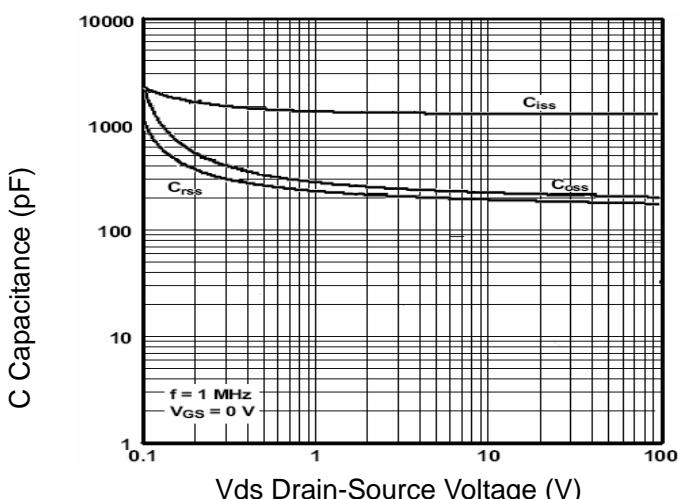


Figure 7 Capacitance vs Vds

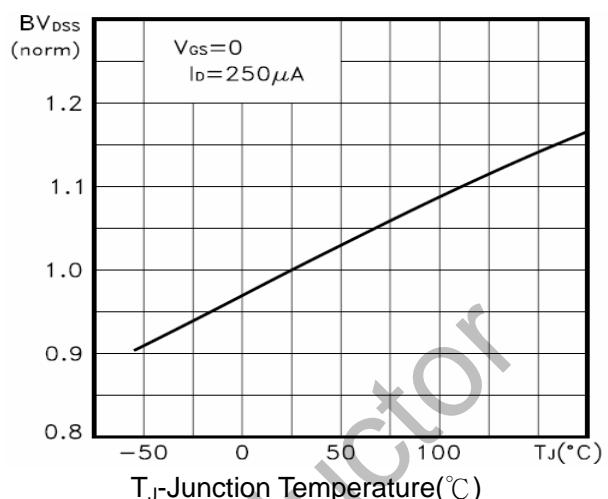
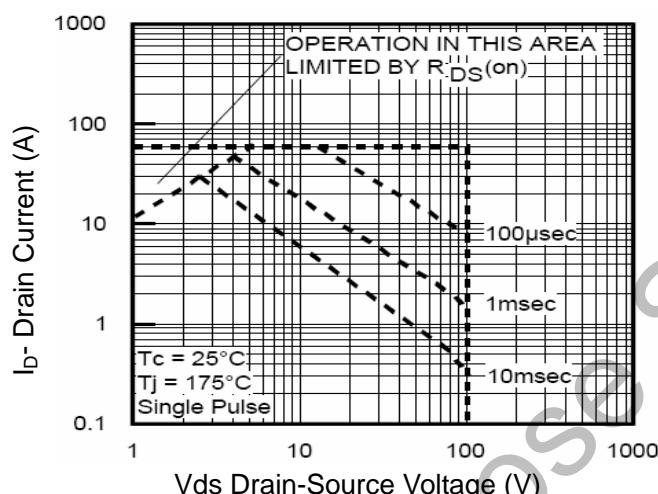
Figure 9 BV_{DSS} vs Junction Temperature

Figure 8 Safe Operation Area

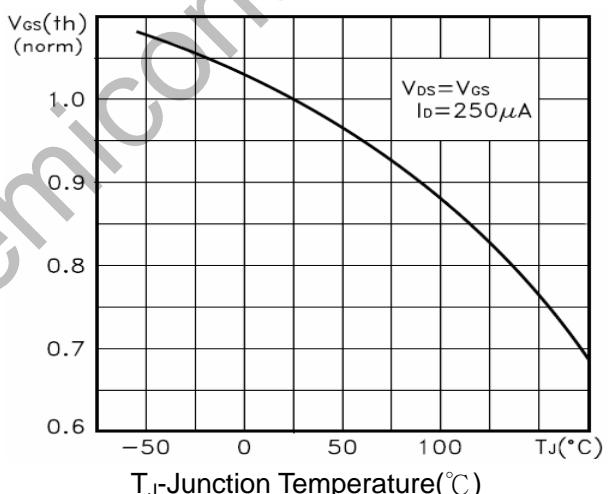
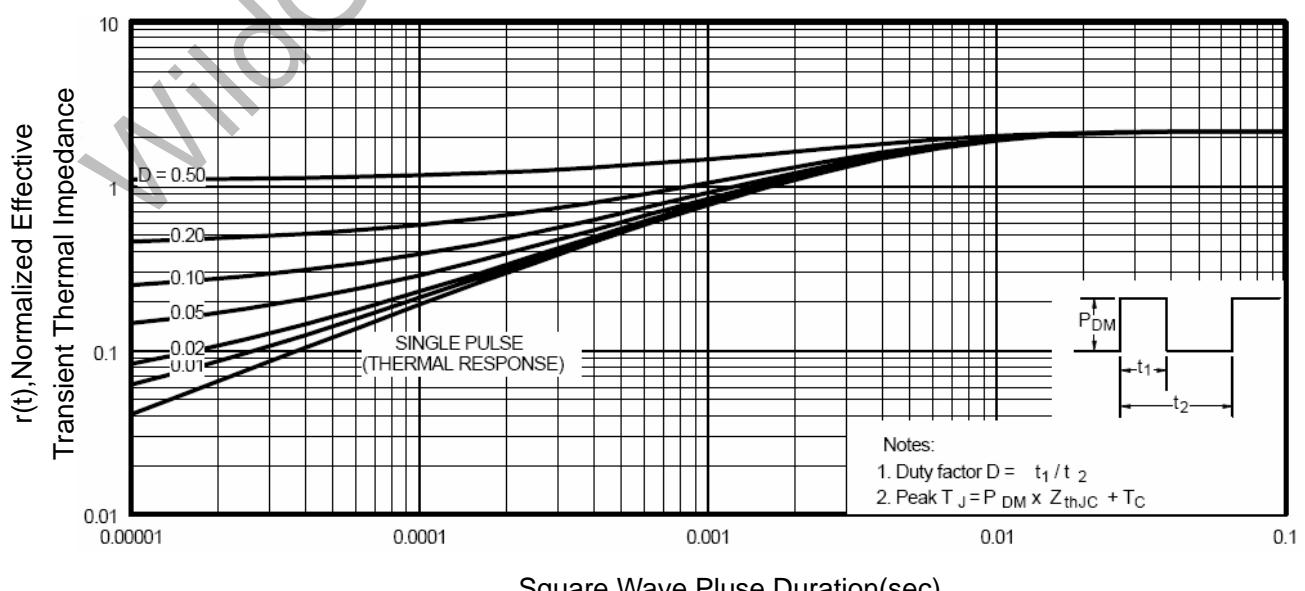
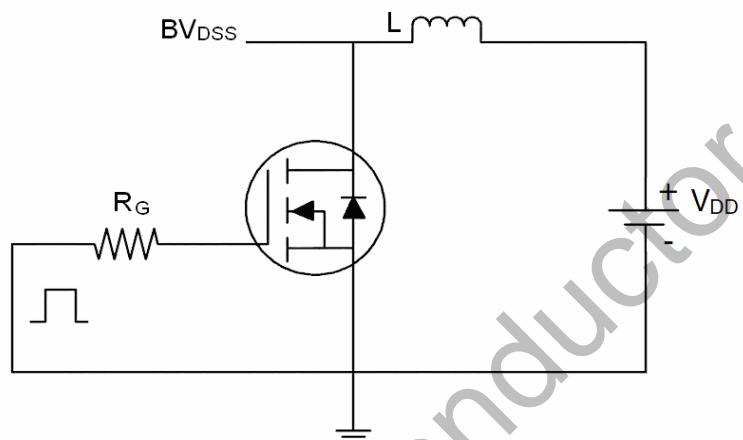
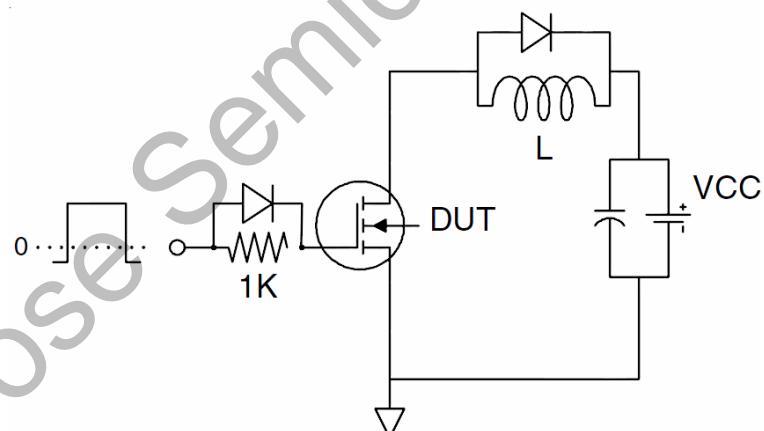
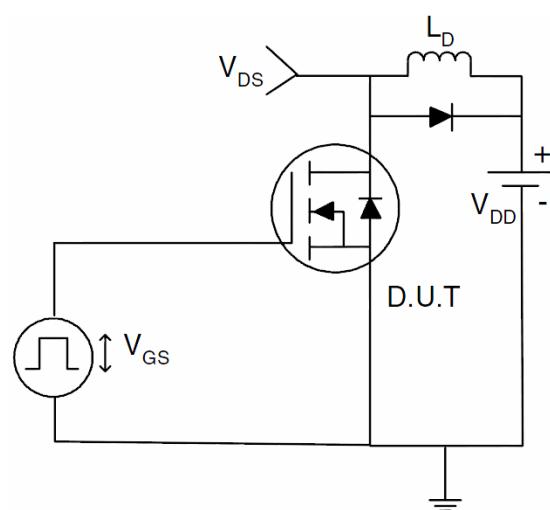
Figure 10 $V_{GS(\text{th})}$ vs Junction Temperature

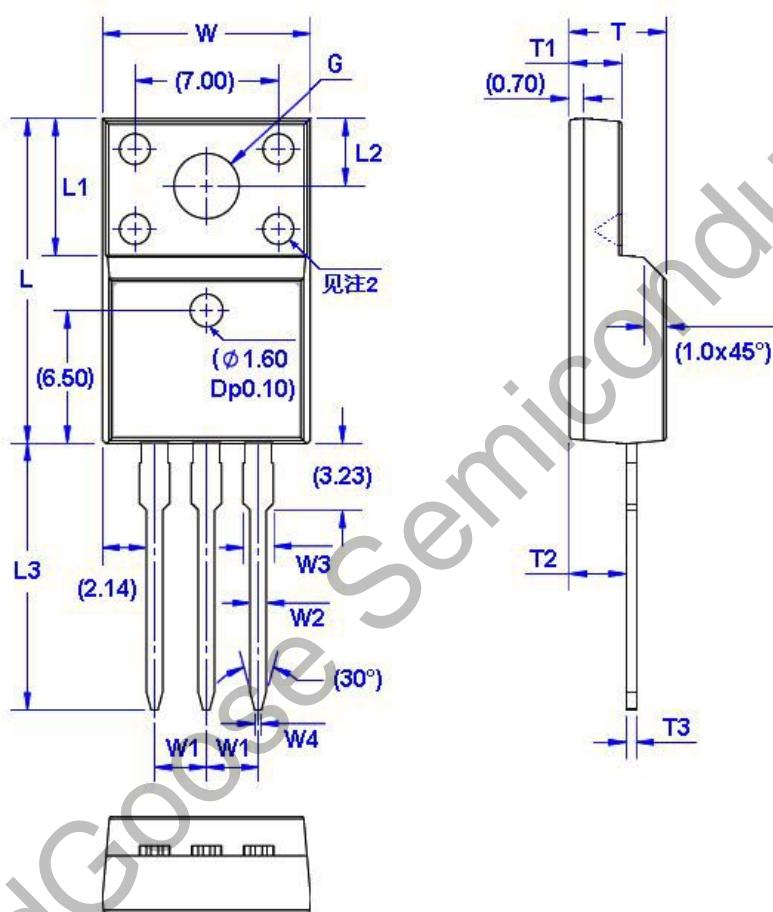
Figure 11 Normalized Maximum Transient Thermal Impedance

Test Circuit**1) E_{AS} test Circuit****2) Gate charge test Circuit****3) Switch Time Test Circuit**

Package Dimension

TO-220F

Unit: mm



Symbol	Size		Symbol	Size		Symbol	Size		Symbol	Size	
	Min	Max		Min	Max		Min	Max		Min	Max
W	9.96	10.36	W4	0.25	0.45	L3	12.78	13.18	T3	0.45	0.60
W1	2.54 (TYP)		L	15.67	16.07	T	4.50	4.90	G(Φ)	3.08	3.28
W2	0.70	0.90	L1	6.48	6.88	T1	2.34	2.74			
W3	1.24	1.47	L2	3.20	3.40	T2	2.56	2.96			