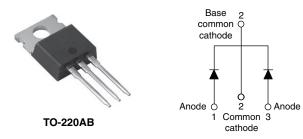


Vishay Semiconductors

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY						
Package	TO-220AB					
I _{F(AV)}	2 x 20 A					
V _R	30 V					
V _F at I _F	0.38 V					
I _{RM} max.	183 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Common cathode					
E _{AS}	13 mJ					

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long
 term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICSL							
SYMBOL CHARACTERISTICS VALUES UNITS							
I _{F(AV)}	Rectangular waveform	40	A				
V _{RRM}		30	V				
I _{FSM}	t _p = 5 μs sine	1100	А				
V _F	20 A_{pk} , T_J = 125 °C (per leg)	0.38	V				
TJ	Range	- 55 to 150	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-42CTQ030PbF	VS-142TQ030-N3	UNITS			
Maximum DC reverse voltage	V _R	V _R 30		V			
Maximum working peak reverse voltage	V _{RWM}		30	v			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS		
Maximum average per leg		50 % duty cycle at T _C = 121 °C	rectangular waveform	20			
See fig. 5 per device	IF(AV)			40	Α		
Maximum peak one cycle non-repetitive surge current per leg		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1100			
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	360			
Non-repetitive avalanche energy per leg		T _J = 25 °C, I _{AS} = 3 A, L = 2.90 mH		13	mJ		
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximu		3	А		

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RoHS

COMPLIANT



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		20 A	T _{.1} = 25 °C	0.48		
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	40 A	1] = 25 0	0.57	V	
	VFM (*)	20 A	T.I = 125 °C	0.38		
		40 A	$I_{\rm J} = 125$ C	0.51		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V - Poted V	3	m۸	
See fig. 2	IRM (")	T _J = 125 °C	$V_R = Rated V_R$	183	ma	
Threshold voltage	V _{F(TO)}	T T maximum		0.22	V	
Forward slope resistance	r _t	$T_J = T_J$ maximum		6.76	0.48 0.57 0.38 0.51 3 183 0.22 V 6.76 mΩ 2840 pF 8.0 nH	
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range	ge 100 kHz to 1 MHz) 25 °C	2840	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 m	m from package body	8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

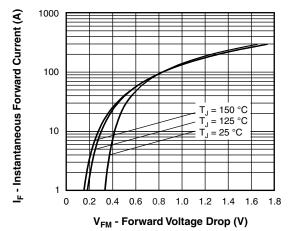
Note

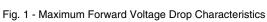
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS		
Maximum junction and storage	ge	T _J , T _{Stg}		- 55 to 150	°C		
Maximum thermal resistance junction to case per leg	3	Р	DC eneration	2.0			
Maximum thermal resistance junction to case per package		R _{thJC}	DC operation	1.0	°C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	R _{thCS} Mounting surface, smooth and greased				
Approvimate weight				2	g		
Approximate weight	Approximate weight			0.07	oz.		
Mounting torque	minimum			6 (5)	kgf ⋅cm		
Mounting torque	maximum			12 (10)	(lbf ⋅ in)		
Marking device			Case style TO-220AB	42CT	Q030		



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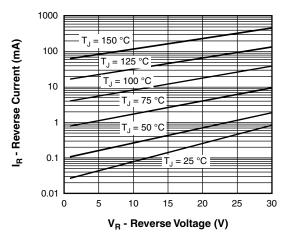


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

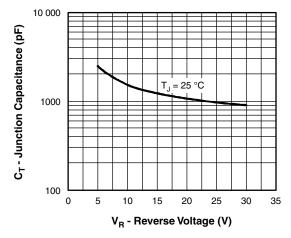
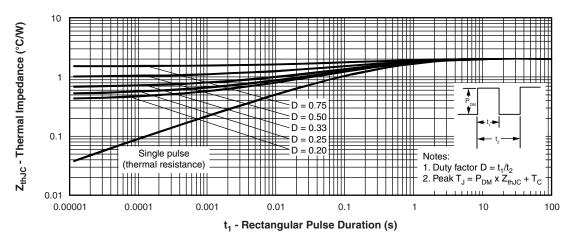


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

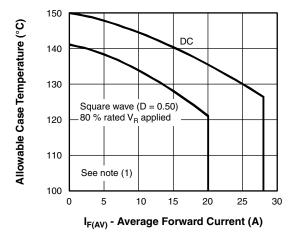


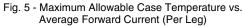


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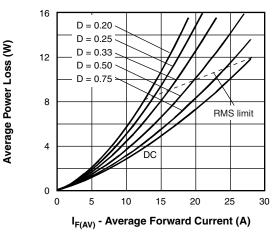


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

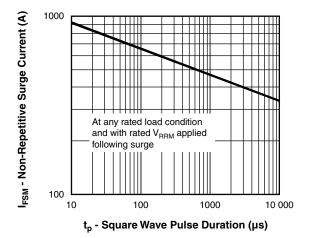


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

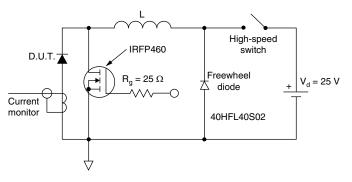


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

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Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	42	С	т	Q	030	PbF
	1	2	3	4	5	6	7
1	-	-	Semico	onductor (40 A)	s produ	ct	
3	-	Circuit configuration					
4	-	C = Common cathode Package					
		T = TC)-220				
5	-	Schottky "Q" series					
6	-	Voltage	e rating	(030 = 3	30 V)		
7	-	Environmental digit					
		 PbF 	= Lead	(Pb)-fre	e and R	RoHS co	ompliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION							
VS-42CTQ030PbF	50	1000	Antistatic plastic tube				
VS-42CTQ030-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			



Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





.ead	assignments

Diodes

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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